

Electronic Acknowledgement Receipt

EFS ID:	1441082
Application Number:	10715629
International Application Number:	
Confirmation Number:	4322
Title of Invention:	Digital signal processor architecture with optimized memory access for code discontinuity
First Named Inventor/Applicant Name:	Frederic Boutaud
Customer Number:	55740
Filer:	Matthew Edward Connors/Elizabeth Ball
Filer Authorized By:	Matthew Edward Connors
Attorney Docket Number:	7020
Receipt Date:	16-JAN-2007
Filing Date:	17-NOV-2003
Time Stamp:	15:18:17
Application Type:	Utility

Payment information:

Submitted with Payment	yes
Payment was successfully received in RAM	\$500
RAM confirmation Number	151
Deposit Account	190079

The Director of the USPTO is hereby authorized to charge indicated fees and credit any overpayment as follows:
Charge any Additional Fees required under 37 C.F.R. Section 1.16 and 1.17

File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)
1	Appeal Brief Filed	appealbrief.pdf	1271404	no	34

Warnings:

Information:

2	Fee Worksheet (PTO-06)	fee-info.pdf	8197	no	2
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Warnings:

Information:

Total Files Size (in bytes):	1279601
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New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.